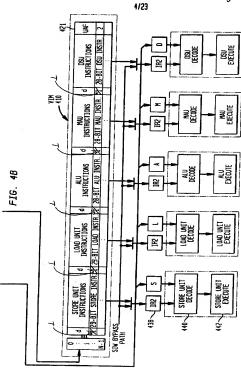
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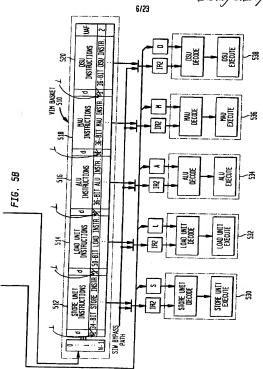
SERIAL NO.: Annotated Sheet PETER H. PRIEST 1919-806-1600) Showing Changes



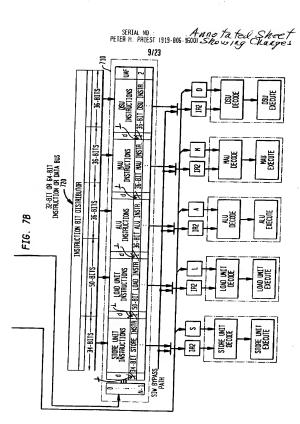
PAGE 18/25 * RCVD AT 8/21/2006 11:14:55 AM [Eastern Daylight Time] * SVR:USPTO-EFXRF-6/42 * DNIS:2738300 * CSID:9198061690 * DURATION (mm-ss):05-46

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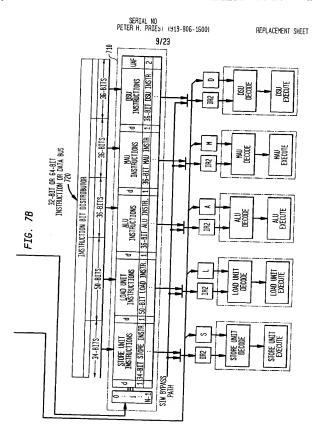


SERIAL NO.: PETER H. PRIEST (5.3-806-1600) REPLACEMENT SHEET 6/23 36-BIT DSU INSTR DSU INSTRUCTIONS DSU EXECUTE E00E VIM BASKET 7 510 36-BIT MAU INSTR. MAU INSTRUCTIONS EXECUTE MAN DECODE 23 ₹ 35-BIT ALU INSTR INSTRUCTIONS EXECUTE PECODE DECODE FIG. 5B 50-BIT LOAD INSTR. LOAD UNIT INSTRUCTIONS OAD UNIT DECODE LOAD UNIT EXECUTE P STORE INSTR. STORE UNIT INSTRUCTIONS STORE UNIT DECODE STORE UNIT EXECUTE ZÍS SIW BYPASS PATH

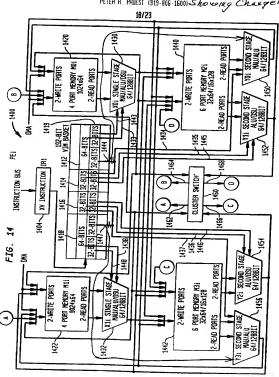


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SERIAL NO.: PETER H. PRIES: (919-806-1600) REPLACEMENT SHEFT 18/23 SECOND STAGE MAU/ALU 64/128BIT 7 1430 1420 2-READ PORTS I SINGLE STAGE FORT MEMORY MOI 1024x64 6 PORT MEMORY NZI 32x64/16x128 2-WRITE PORTS 2-READ PORTS 2-WRITE PORTS 64/128811 -SECOND STAGE ALU/DSU 64/128BIT 2-READ PORTS ē 1400 0 1419 192-BIT VIM BASKET -1435 -1445 **64-811S** 32-BITS 32--1444 55 紐 1412 IR1 32-8ITS 32-BTB INSTRUCTION BUS XV INSTRUCTION CLUSTER SWITCH" 8 32-BITS 32-BITS 32-8ITS 32-BITS ₹ 6 64-BITS -1454 FIG. SECOND STAGE ž 1448 2-READ PORTS PORT MEMORY M1i 1024x64 SINGLE STAGE 6 PORT NEMORY N31 32x64/16x128 2-WRITE PORTS AU/ALU/DSU 64/128BTT 2-READ PORTS 2-WRITE PORTS 2-READ PORTS SECOND STAGE NAU/ALU 64/129817 145 1422~ 1432-

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